

ELECTRIC DOUUBLE LAYER CAPACITORS

PRODUCT SPECIFICATION 規格書

CUSTOMER: DATE:

(客戶): (日期):2018-02-26

CATEGORY (品名) : ELECTRIC DOUBLE LAYER CAPACITORS

DESCRIPTION (型号) : DRL 3V1F (φ8x12)

VERSION (版本) : 01

Customer P/N : /

SUPPLIER : /

SUPPLIER					
PREPARED (拟定)	CHECKED (审核)				
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CUSTOMER				
APPROVAL (批准)	SIGNATURE (签名)			

		SPECIFICAT	TION		ALTERN	ATION HIST	ΓORY
	DRL SERIES				R	ECORDS	
Rev.	Date	Mark	Page	Contents	Purpose	Drafter	Approver

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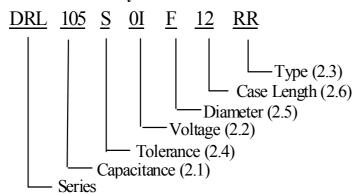
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1. Application

The specification applies to electric double layer capacitors used in electronic equipment.

2. Part Number System



2.1 <u>Capacitance code</u>

Code	105
Capacitance (F)	1

2.2 Rated voltage code

Code	01
Voltage (W.V.)	3

2.3 <u>Type</u>

Code	RR
Type	Bulk

2.4 <u>Capacitance tolerance</u>

"S" stands for $-20\% \sim +50\%$

2.5 Diameter

Code	F
Diameter	8

2.6 <u>Case length</u> 12=12mm

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3. Characteristics

Standard atmospheric conditions

Unless otherwise specified, the standard range of atmospheric conditions for making measurements and tests is as follows:

Ambient temperature: 15°C to 35°C Relative humidity : 25% to 75% Air Pressure : 86kPa to 106kPa

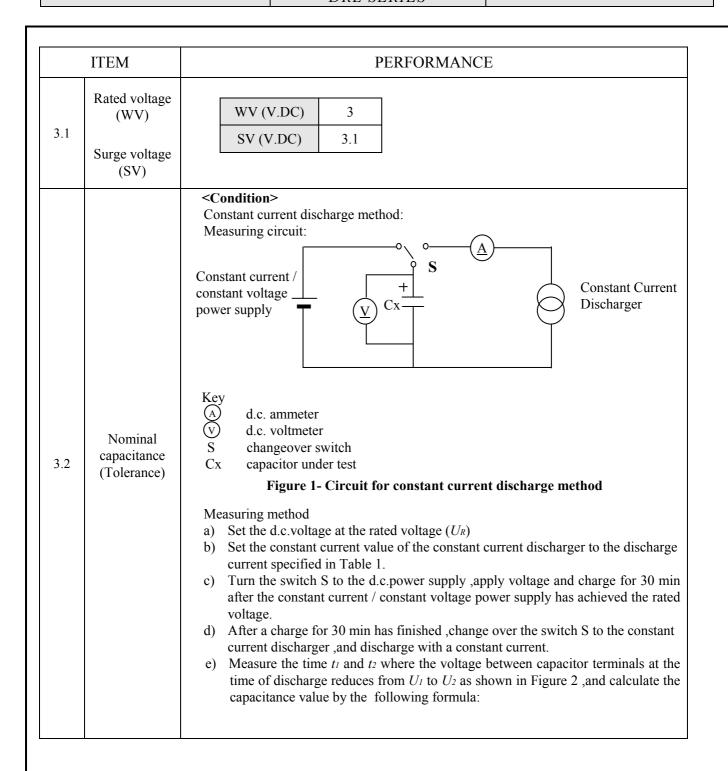
If there is any doubt about the results, measurement shall be made within the following conditions:

Ambient temperature: $20^{\circ}\text{C} \pm 2^{\circ}\text{C}$ Relative humidity : 60% to 70%Air Pressure : 86kPa to 106kPa

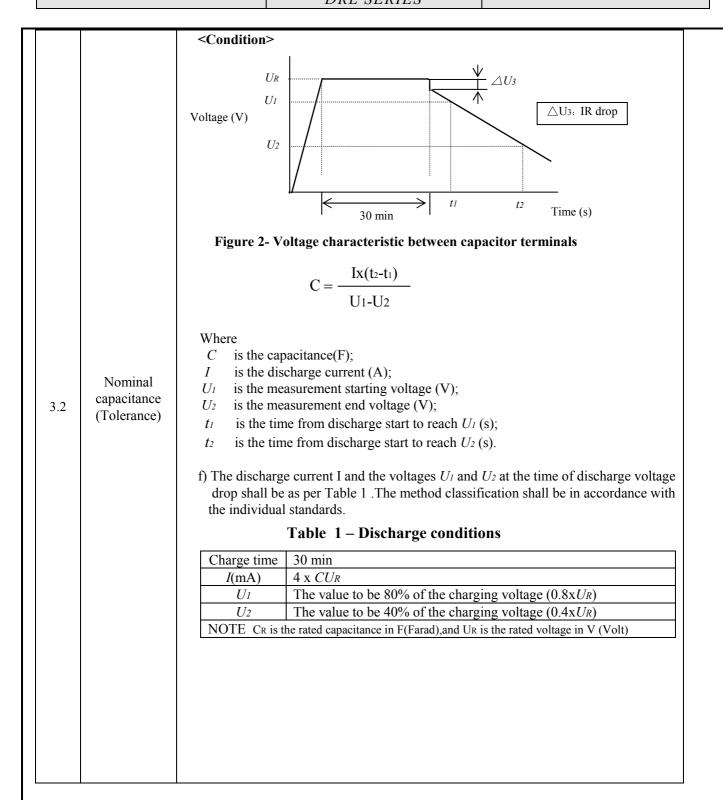
Operating temperature range

The ambient temperature range at which the capacitor can be operated continuously at rated voltage is -40°C to 60°C.

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3.3	ESR	<pre> <condition> Measuring frequency :1kHz Measuring temperature:25±2°C Measuring point : 2mm max from the surface of a sealing resin on the lead wire. <criteria> (25°C)Less than the initial limit: $ESR \le 200 m\Omega$</criteria></condition></pre>							
3.4	Leakage current	<condition> 1. Ambient temperature: 25°C ± 2°C. 2. The electrification time:72H 3. Desistance value of protective resistor less than 1 Ω. <criteria> Less than the initial limit(25°C ± 2°C): I≤0.008mA I is the Leakage current</criteria></condition>							
3.5	Temperature characteristic		Temperature(°C) 20±2 -40+3 Keep at 15 to 35°C for 15 minutes or more 60±2 °C/ESR 20°C: ESR ratio at 0°C: Capacitance change;	Item Capacitance ESR △C/C ESR △C/C ESR AC/C Item AC/C ESR	Characteristics Within ±30% of initial capacitance Less than or equal to 4 times of the value of item 3.3 Within ±30% of initial capacitance The limit specified in 3.3				

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		<criteria></criteria>	
		Item	Performance
		Capacitance Change	Within ±30% of initial capacitance
3.6	Load life	ESR	Less than or equal to 4 times of the value of item 3.3
3.0	test	Appearance	No visible damage and no leakage of electrolyte
		<condition></condition>	
			exposed for 240±48 hours in an atmosphere of 90~95%RH a
		Humidity Test: The capacitor shall be 40±2°C, the characteri	exposed for 240±48 hours in an atmosphere of 90~95%RH a stic change shall meet the following requirement.
		Humidity Test: The capacitor shall be	
	Damp	Humidity Test: The capacitor shall be 40±2°C, the characteri	stic change shall meet the following requirement.
3.7	Damp heat test	Humidity Test: The capacitor shall be 40±2°C, the characteri Criteria> Item	stic change shall meet the following requirement. Performance

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		a) Lead pull strength A static load force shall be ap in a direction away from the	•	minal in the axial direction and acting
		Lead wire diameter		Load force (N)
		d ≤0.5	,	5
3.8	Lead strength	table above is applied to one le horizontal position and then re for 2~3 seconds. The additional bends are made Lead wire diameter (n d≤0.5	ead and then the turned to a vertee in the opposition (in shall meet the distribution). The performance within ±30%	Load force (N) 2.5 he following value after a) or b) test. of initial capacitance mage Legible marking and no
3.9	Resistance to vibration	Performance: Capacitance value she capacitance when the value is mean	ig2 all not show of sured within 3	

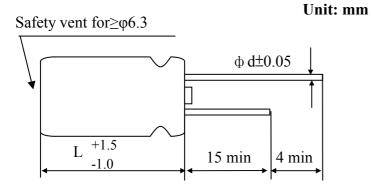
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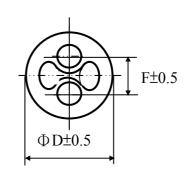
3.10	Solderability	The capacitor shall be tested under the following conditions: Solder : Sn-3Ag-0.5Cu Soldering temperature: 245±3°C Immersing time : 2.0±0.5s Immersing depth : 1.5~ 2.0mm from the root. Flux : Approx .25% rosin Performance: At least 75% of the dipped portion of the terminal shall be covered with new solder.
3.11	Resistance to soldering heat	A) Solder bath method Lead terminals of a capacitor are placed on the heat isolation board with thickness of 1.6±0.5mm. It will dip into the flux of isopropylaehol solution of colophony. Then it will be immersed at the surface of the solder with the following condition: Solder : Sn-3Ag-0.5Cu Soldering temperature : 260 ±5°C Immersing time : 5±0.5s Heat protector: t=1.6mm glass -epoxy board B) Soldering iron method Bit temperature : 350 ±10°C Application time : 3.5 ±0.5 s Heat protector: t=1.6mm glass -epoxy board For both methods, after the capacitor at thermal stability, the following items shall be measured: Item Performance Capacitance Change Within ±10% of initial capacitance Appearance No visible damage legible marking and no leakage of electrolyte

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4. Product Dimensions





φD	8
L	12
F	3.5
φd	0.5

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- (1) The capacitor has fixed polarity.
- (2) The capacitor should be used under rated voltage.
- (3) The capacitor should not be used in the charge and discharge circuit with high frequency.
- (4) The ambient temperature affects the super capacitor life.
- (5) Voltage reduction $\Delta V=IR$ will happen at the moment of discharge.
- (6) The capacitor cannot be stored on the place with humidity over 85%RH or place with toxic gas.
- (7) The capacitor should stored in the environment within $-30^{\circ}\text{C} \sim 50^{\circ}\text{C}$ temperature and less than 60% relative humidity.
- (8) If the capacitor is applied on the double-side PCB, the connection should not be around the place on which the super capacitor can contact.
- (9) Don't twist capacitor or make it slanting after installing.
- (10) Need avoid over heat on the capacitor during soldering (The temperature should be 260°C with the time less than 5s during soldering on 1.6mm printed PCB.)
- (11) There is voltage balance problem between each capacitor unit during series connection between super capacitor.

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